

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of : Computer Motherboard Architecture
with Integrated DSP for Continuous
and Command and Control Speech
Processing

Serial No. : 09/677,569

Filing Date : October 12, 2000

Examiner/Art Unit : Unknown

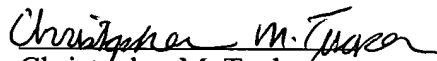
Information Disclosure Statement

Commissioner of Patent and Trademarks
Washington, D.C. 20231

Sir:

Please find enclosed herewith an Information Disclosure Statement for the above
application along with a copy of the cited art.

Respectfully submitted,


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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. 	
In Re Application Of: CYBERNAUT CORPORATION				
Serial No. 09/677,569	Filing Date 10/2/01	Examiner 	Group Art Unit 2183	
Title: COMPUTER MOTHERBOARD ARCHITECTURE WITH INTEGRATED DSP				

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Address to:
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Washington, D.C. 20231

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

Payment of Fee
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☐ A check in the amount of _____ is attached.

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James J. Ralabate

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Dated: **OCTOBER 2, 2001**

CERTIFICATE OF MAILING BY EXPRESS MAIL

I certify that this Information Disclosure Statement and related filing documents are being deposited on October 2, 2001 with the U.S. Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R.1.10 and is addressed to the Commissioner of Patents and Trademarks, Washington, DC 20231.

Signature: *James J. Ralabate*

Person Mailing Correspondence: **JAMES J. RALABATE**

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